

ISL69147

Digital Dual Output 7-Phase AMD PWM Controller

FN8706
Rev. 4.00
Dec 11, 2017

The [ISL69147](#) is a digital dual output, multiphase PWM controller designed to be compliant with AMD SVI2 specifications. The digital multiphase controller can be configured to support any desired phase assignments up to a maximum of seven phases across the two outputs (X and Y). For example, 6+1, 5+2, 4+2, 3+3, 3+2, or even a single output operation as a 7+0 configuration are supported. The ISL69147, with a flexible $X+Y \leq 7$ phase assignment, supports the SVI2 interface and the PMBus v1.3 interface, making it ideal for controlling the microprocessor core, memory, and system rails for AMD SVI2 based platforms.

The ISL69147 uses Intersil's proprietary digital linear synthetic current modulation scheme to achieve the industry's best combination of transient response and ease of tuning while addressing the challenges of powering the latest generation of AMD microprocessors. Configuration is accomplished through Intersil's intuitive PowerNavigator™ GUI. Diode emulation and automatic phase add/drop features allow the user to extract maximum efficiency from the converter regardless of load conditions.

The ISL69147 supports a comprehensive fault management system to enable the design of highly reliable systems. From an overcurrent protection scheme including peak and average detection, to the configurable power-good and catastrophic fault protection flags, almost any need is accommodated.

With minimal external components, the ability to store eight configurations, robust fault management, and highly accurate regulation capability, implementing a high performance multiphase regulator has never been easier.

Applications

- Core and graphics for AMD SVI2 based processors
 - High performance servers core rail
 - High performance graphic rail
 - High-end desktop with overclocking option
- Networking, data center, storage, and general purpose

Features

- Advanced linear digital modulation scheme
 - Zero latency synthetic current control for excellent high frequency current balance
 - Auto phase add/drop for excellent load vs efficiency profile
 - Excellent DVID performance
 - Dual edge modulation for faster transient response
- Up to 1MHz operation for high density designs
- Diode braking for overshoot reduction
- Diode emulation for enhanced light-load efficiency
- Differential remote voltage sensing supports $\pm 0.5\%$ closed-loop system accuracy over load, line, and temperature
- Highly accurate current sensing for excellent load line regulation and accurate OCP
 - Supports ISL99227 60A smart power stages
 - Supports DCR sense with integrated temperature compensation
- Supports phase doubler (ISL6617A) for up to 14-phase operation
- Comprehensive fault management enables high reliability systems
 - Pulse-by-pulse phase current limiting
 - Total output current protection
 - Output and input OV/UV protection
 - Open voltage sense detect
 - Black box recording capability for faults
 - Configurable catastrophic failure flag output (CFP)
- Intuitive configuration using [PowerNavigator](#)
- SMBus/PMBus v1.3 compatible
 - Up to 2MHz bus interface
 - NVM to store up to eight configurations
- Pb-free (RoHS compliant)

**© Copyright Intersil Americas LLC 2016-2017. All Rights Reserved.
All trademarks and registered trademarks are the property of their respective owners.**

For additional products, see www.intersil.com/en/products.html

Intersil products are manufactured, assembled and tested utilizing ISO9001 quality systems as noted in the quality certifications found at www.intersil.com/en/support/qualandreliability.html

Intersil products are sold by description only. Intersil may modify the circuit design and/or specifications of products at any time without notice, provided that such modification does not, in Intersil's sole judgment, affect the form, fit or function of the product. Accordingly, the reader is cautioned to verify that datasheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com