

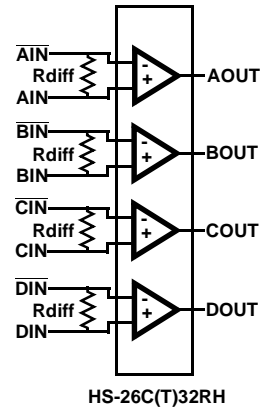
## Description

In failsafe mode the inputs to the HS-26C(T)32RH quad differential line receiver are open circuited and not driven. A designed imbalance in the input resistor structure generates an inherent error voltage, which forces the outputs high. The magnitude of the error voltage is a function of the differential impedance between the input lines. If this device is to be operated in failsafe mode, we recommend testing to establish the LET threshold for SEU.

For application assistance on this or any other Intersil radiation hardened ICs, please feel free to contact us at the web address shown below.

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## Schematic



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