Plastic Packages for Integrated Circuits

Package Outline Drawing

P5.064A
5 LEAD SMALL OUTLINE TRANSISTOR PLASTIC PACKAGE
Rev 0, 2/10

NOTES:
1. Dimensions are in millimeters. Dimensions in ( ) for Reference Only.
3. Dimension is exclusive of mold flash, protrusions or gate burrs.
4. Foot length is measured at reference to guage plane.
5. This dimension is measured at Datum "H".
6. Package conforms to JEDEC MO-178AA.

TYPICAL RECOMMENDED LAND PATTERN